SYSTEM AND METHOD FOR OPTIMIZING POWER/PERFORMANCE IN NETWORK CENTRIC MICROPROCESSOR CONTROLLED DEVICES

Abstract of the Disclosure

5   A method and system for controlling the speed of a microprocessor in a microprocessor-controlled device when executing an application program retrieved from a remote repository. The program retrieved contains embedded instructions related to the processing power required to execute the program and maintain a specified quality of performance. The processor speed is adjusted consistent with the

10   embedded instructions so that when necessary, the processor can function at a higher speed, consuming greater power, and when not necessary, the processor can return to consuming lower power at a low-speed state, to thereby conserve battery resources.